

PATENT NUMBER

ISSUE CLASSIFICATION	
Class	Subclass

O.I.P.E. SCANNED <u>AK</u> Q.A. <u>EW</u>	PATENT DATE
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APPLICATION NO. 09/802120	CONT/PRIOR D	CLASS 712	SUBCLASS 24	ART UNIT 2183	EXAMINER <u>Cham Eddie</u>
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VLIW computer processing architecture having the program counter stored in a register file register

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
Amount Due			Date Paid	
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<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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